PTO/SB/08A (08-03)

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Sheet 1

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

of 2

(Use as many sheets as necessary)

Complete if Known			
Application Number	10/790,522		
Filing Date	03/01/2004		
First Named Inventor	Seth M. Demsey		
Art Unit	2154		
Examiner Name	Unknown		
Attorney Docket Number	MS306914.1/MSFTP571US		

			U. S. PATEN	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number  Number-Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/P.F./		US- 6,367,034	04-02-2002	Novik et al.	
1		<sup>US-</sup> 6,314,533	11-06-2001	Novik et al.	-
1/		<sup>US-</sup> 6,275,957	08-04-2001	Novik et al.	
V		<sup>US-</sup> 5,822,758	10-13-1998	Loper et al.	
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	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages		
		Country Code <sup>3</sup> "Number <sup>4</sup> "Kind Code <sup>8</sup> (if known)	MM-DD-YYYY		Or Relevant Figures Appear	r°	
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Examiner Signature	/Parham Fatehi/	Date Considered	09/24/2007

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(Use as many sheets as necessary)				Art Unit	2154			
,,,		Examiner Name	Unknown					
Sheet	2	of	2	Attorney Docket Number	MS306914.1/MSFTP571US	. /		

Examiner Initials*	Cite No. <sup>1</sup>	NON PATENT LITERATURE DOCUMENTS  Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
/P.F./		HOWARD CHEN, et al., Dynamic Trace Selection Using Performance Monitoring Hardware Sampling, Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization, 2003, pp. 79-90, San Francisco, California, USA.		
1		EHAB AL-SHAER, et al., Adaptive Object-Oriented Filtering Framework for Event Management Applications, ACM Computing Surveys, 2000, Vol. 32-Issue 1es.		
	:	MATTHEW FARRENS, et al., Dynamic Base Register Caching: A Technique for Reducing Address Bus Width, Proceedings of the 18th annual international symposium on Computer architecture, 1991, pp.128-137, Toronto, Ontario, Canada.		
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Examiner		Date	
	/Parham Fatehi/		09/24/2007
Signature	ri arriani i atem/	Considered	0312412001 .

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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